METHODOLOGY / PLANNING OF WORK.


STEP 2. Study the Design concepts of CMOS exiting technology in wireless Networks

The three essential parameters for the design of a Wireless Area Networks:
Data Rate / Handling Frequency. (G-bit/Sec)
Density of Components. (Size)
Power (M-Watts)


2. Calculation Various Electrical parameters which is effective to power optimization:

3. Calculation of the Area of Wireless Personal Area Network:

4. Calculation of Density of Wireless Personal Area Network

5. Determination of Power:
Actual Calculation of CMOS & Bi-CMOS technology. Using Tanner EDA tools for Simulation Level in WPAN .


STEP 4. Observing the results of Power Optimization in WPAN impact on account of modifying the Design Network.
STEP 5. Fabrication of the simulated Wireless Personal Area Network. After the modeling of the proposed WPAN and achieving satisfactory results we came to our fabrication part.

STEP 6. Finally testing the fabricated WPAN to verify the required result.