OBJECTIVE OF THE PROPOSED RESEARCH.

The objective of my work is to optimization in Power Consumption in Wireless Area Network its drawback in existing system. To work on reduction of its size. The techniques like Bi-CMOS and CMOS technology such to improve electrical parameter. Using Electronics EDA tools and Tanner simulation tools. Software .transmission line model, simulation will be done for power optimization of components as well as system with maintain Bandwidth and speed of Wireless communication in Personal Area Network will be improved also for size reduction,

• Study and analysis of various Wireless technology and techniques available in the literature for copyright protection of low power Wireless.
• Study of wireless Personal Area Network techniques implemented using Electronics Design Tool. Also Considering Various Power like Static and Dynamic electrical Parameter which techniques for improvement in performance.
• Study and analysis of CMOS and Bi- CMOS technology for enhancement in Low power and High Data Rate Communication.
• Design of effective Wireless Personal Area Network using Tanner EDA Tools for Simulation.
• To extend the design of Wireless Personal Area Network using high data payload and maintain the improved performance.
• Experimental validation and analysis of the above designed power optimization in Wireless Personal area Network.